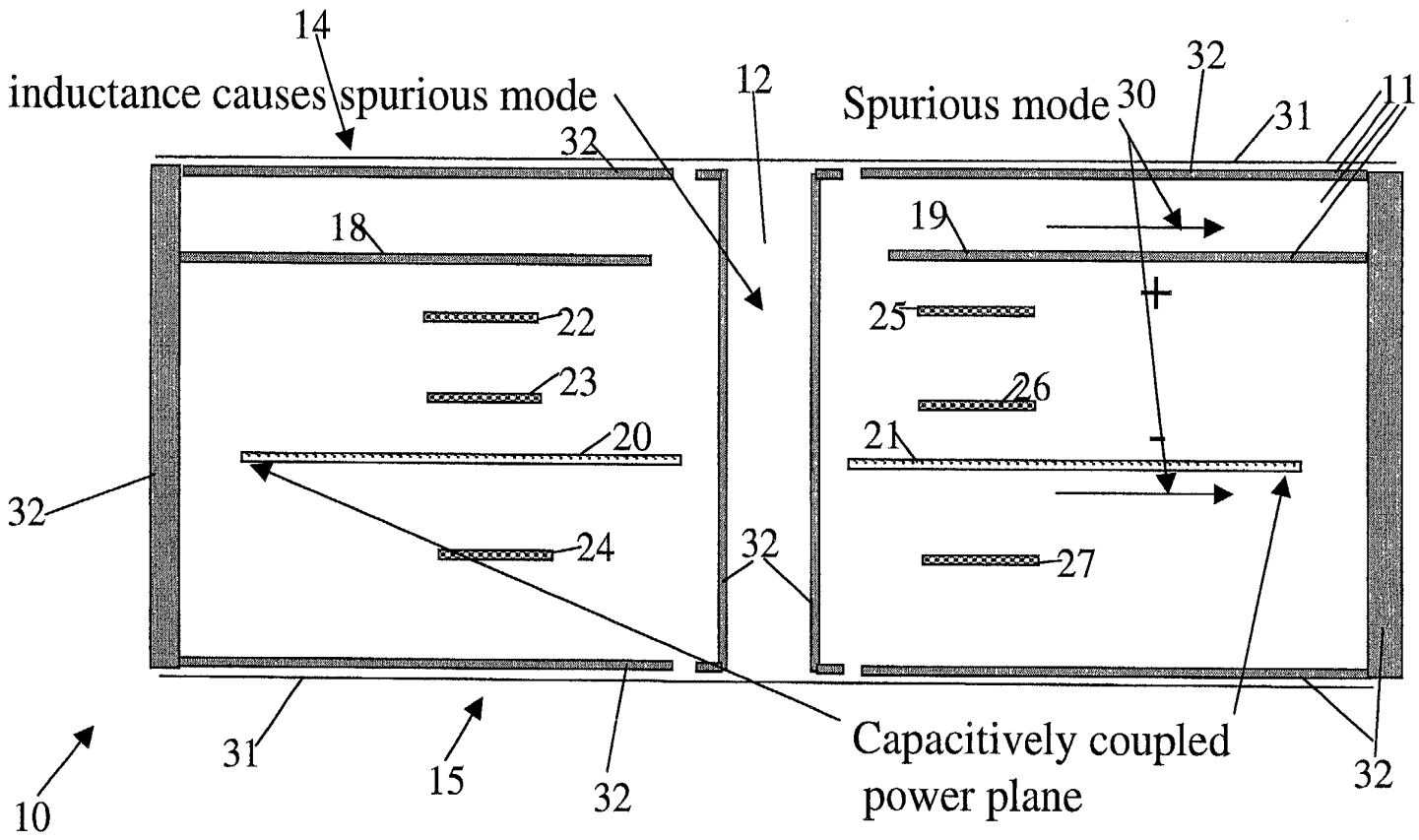
TO2090 "hTE97860



Prior Art

Figure 1

TO2090 PCT/EP97/06860

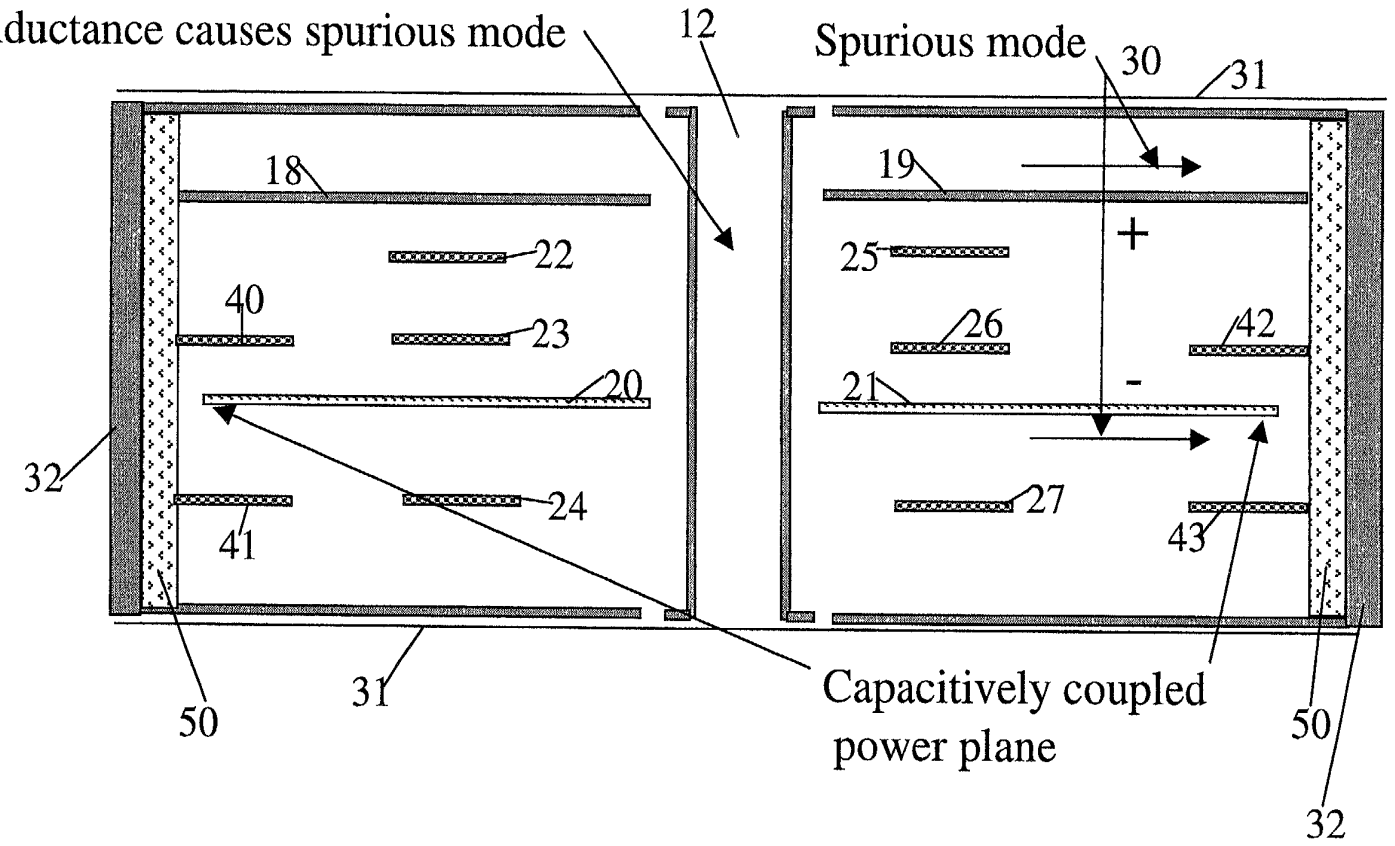
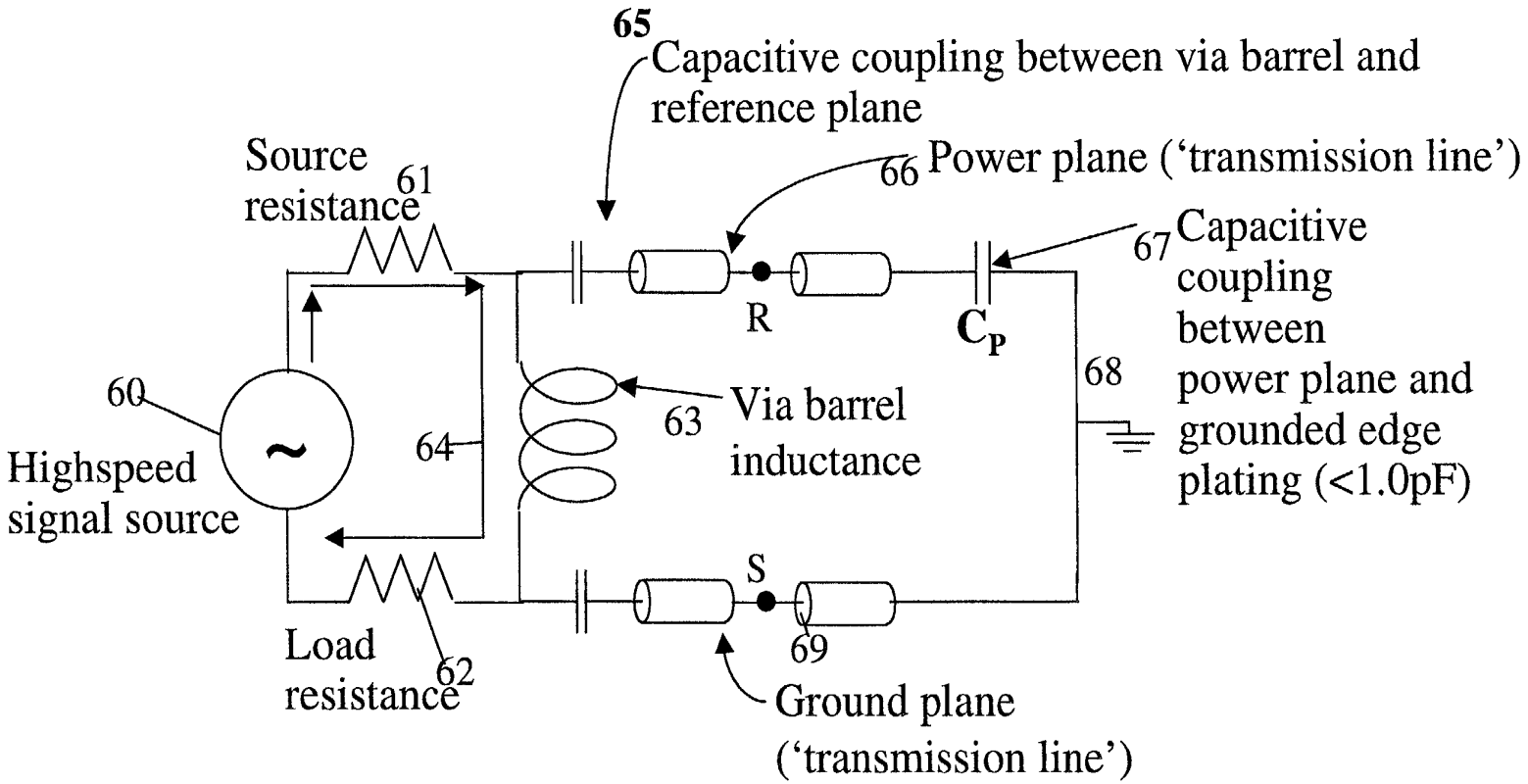


Figure 2



Prior Art

Figure 3

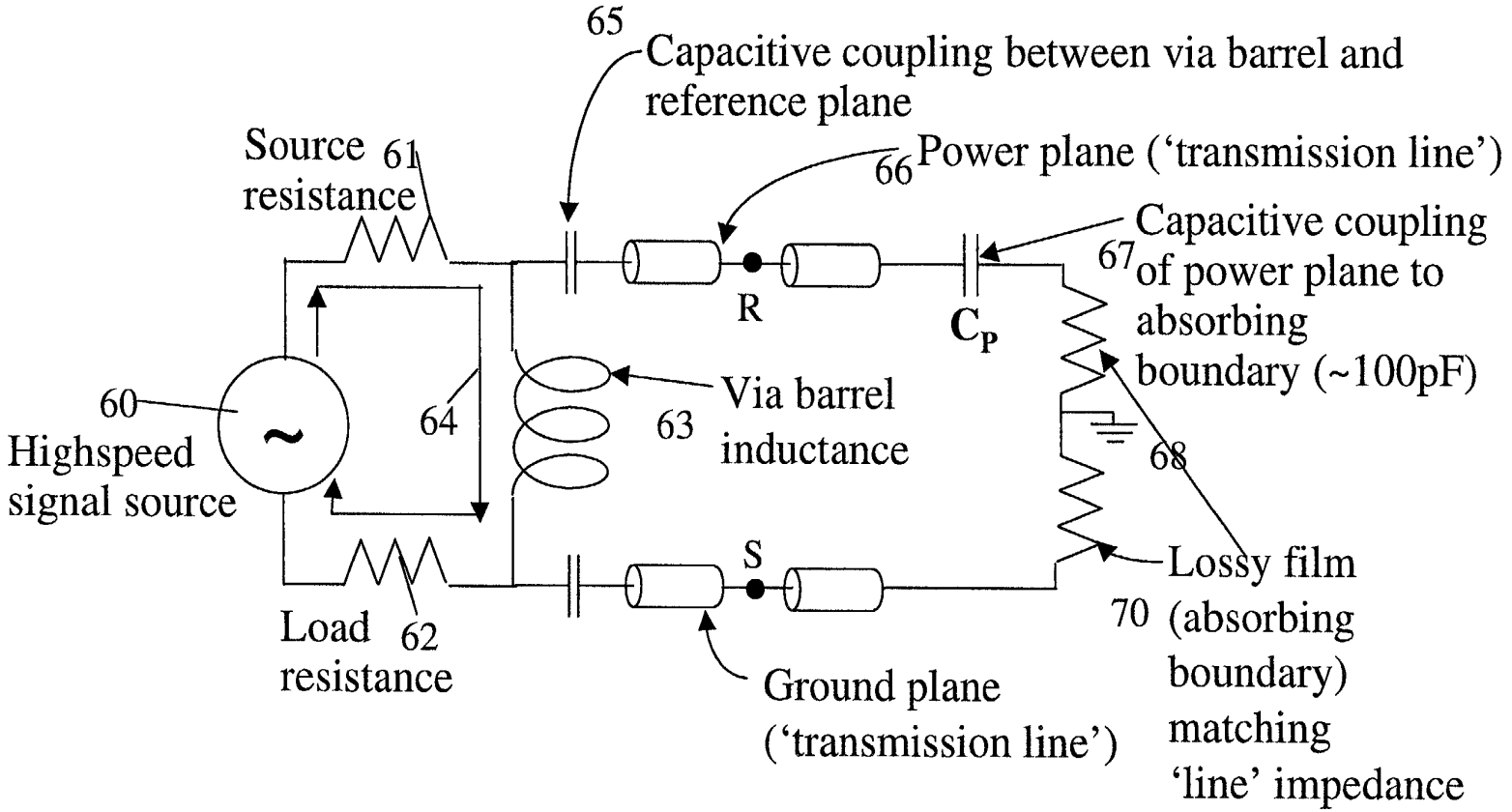

65
Capacitive coupling between via barrel and reference plane
Source resistance
61
66
Power plane ('transmission line')
Capacitive coupling
67
of power plane to absorbing boundary (~100pF)
R
C_P
60
Highspeed signal source
64
63
Via barrel inductance
68
S
Lossy film
70
(absorbing boundary) matching 'line' impedance
Load resistance
62
Ground plane ('transmission line')

Figure 4

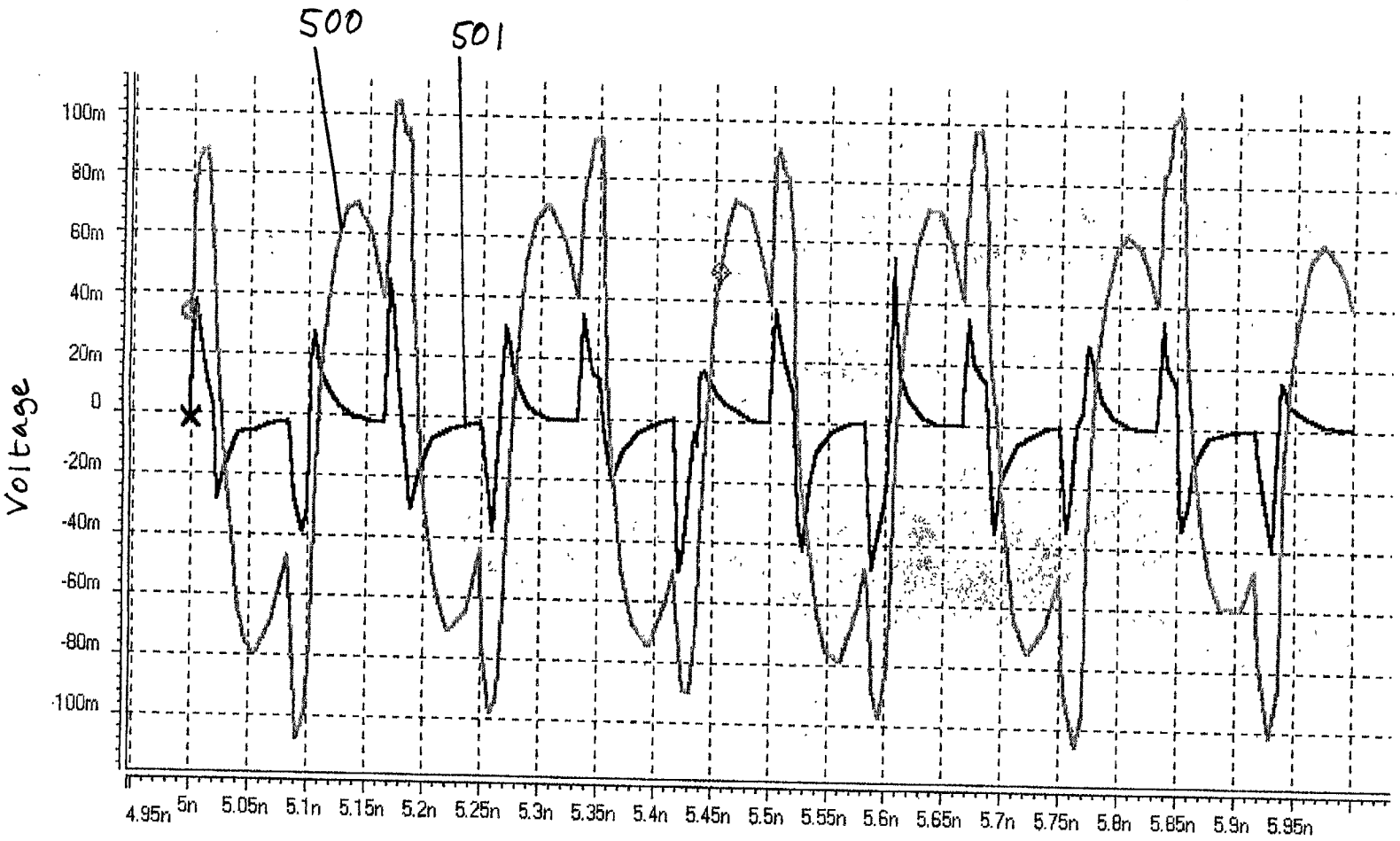

500
501
100m
80m
60m
40m
20m
0
-20m
-40m
-60m
-80m
-100m
Voltage
4.95n
5n
5.05n
5.1n
5.15n
5.2n
5.25n
5.3n
5.35n
5.4n
5.45n
5.5n
5.55n
5.6n
5.65n
5.7n
5.75n
5.8n
5.85n
5.9n
5.95n
Time

FIGURE 5